ELECTRICAL AND COMPUTER ENGINEERING DEPARTMENT, OAKLAND UNIVERSITY ECE-2700: Digital Logic Design

Winter 2021

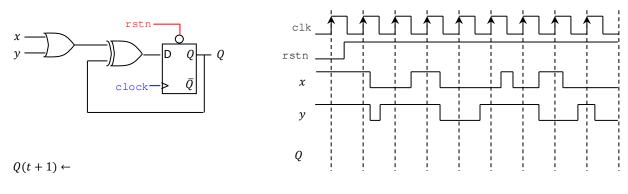
Student Honor Pledge:

All work submitted is completed by me directly without the use of any unauthorized resources or assistance Initials: _____

Quiz 3 (March 23rd @ 5:30 pm)

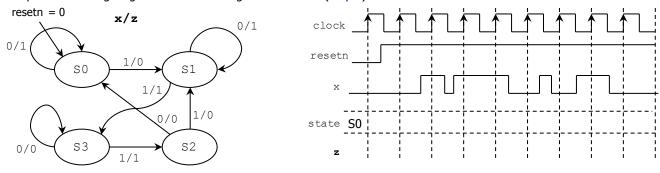
PROBLEM 1 (30 PTS)

• Given the following circuit, complete the timing diagram and get the excitation equation for *Q*.



PROBLEM 2 (35 PTS)

• Complete the timing diagram of the following state machine (30 pts):



• For the given FSM diagram, circle or mark the correct FSM type:

(Mealy) (Moore)

PROBLEM 3 (35 PTS)

• Complete the timing diagram of the following circuit. $Q = Q_3 Q_2 Q_1 Q_0$

